

Type-Based Data Structure Verification

Author information hidden for double-blind review

Abstract

We present a refinement type-based approach for the static verification of complex data structure invariants. Our approach is based on the observation that complex data structures are often fashioned from two elements: recursion (e.g., lists and trees), and maps (e.g., arrays and hash tables). We introduce two novel type-based mechanisms targeted towards these elements: recursive refinements and polymorphic refinements. These mechanisms automate the challenging work of generalizing and instantiating rich universal invariants by piggybacking simple refinement predicates on top of types, and carefully dividing the labor of analysis between the type system and an SMT solver [6]. Further, the mechanisms permit the use of the abstract interpretation framework of liquid type inference [18] to automatically synthesize complex invariants from simple logical qualifiers, thereby almost completely automating the verification. We have implemented our approach in DSOLVE, which uses liquid types to verify OCAML programs. We present experiments that show that our type-based approach reduces the manual annotations required to verify complex properties like sortedness, balancedness, binary-search-ordering, and acyclicity by more than an order of magnitude.

1. Introduction

Recent advances in Satisfiability Modulo Theories (SMT) solving, model checking and abstract interpretation have made it possible to build tools that automatically verify safety properties of large software systems. However, the inability of these tools to automatically reason about data structures severely limits the kinds of properties they can verify. The challenge of automating data structure verification stems from the need to reason about relationships between the unbounded number of values comprising the structure. In SMT and theorem proving based approaches, manual effort is needed to help the prover to *generalize* relationships over specific values into universal facts that hold over the structure, and dually, to *instantiate* quantified facts to obtain relationships over particular values. In model checking and abstract interpretation based approaches, manual effort is needed to design, for each data structure, an abstract domain that is capable of generalizing and instantiating relevant relationships.

Types provide a robust means for reasoning about coarse-grained properties of an unbounded number of values. For example, if a variable x is of the type `int list`, then we are guaranteed that *every* value in the list is an integer. Similarly, if a variable g is of the type `(int, int) Map.t`, then we are guaranteed that g is a hash-map where *every* key is an integer that is mapped to another integer. Modern type systems automatically generalize such global properties from local properties of the individual values added to the structure, and dually, automatically instantiate the properties when the structures are accessed.

In this paper, we show how coarse-grained types can be refined with simple, quantifier-free predicates to yield a framework that is *expressive* enough to enable the specification of a variety of com-

plex data structure properties, yet *structured* enough to enable automatic verification and inference of the properties. We reconcile expressiveness and automation through two novel type refinement mechanisms targeted towards the two elements from which complex structures are fashioned, namely recursion (e.g., lists and trees) and maps (e.g., arrays and hash tables).

The first mechanism, *recursive refinements*, allows the recursive type that encodes a structure to be refined with a matrix of predicates that individually refine the elements of the recursive type. Recursive refinements allow us to express *uniform* properties, for example that a list contains values greater than some program variable i , (abbreviated as $\{\nu: \text{int} \mid i < \nu\}$ list). Further, by *recursively propagating* the matrix when the type is folded or unfolded, the mechanism allows us to algorithmically analyze rich *nested* properties like sortedness, binary-search-ordering and balancedness.

The second mechanism, *polymorphic refinements*, allows the polymorphic type schemes of map data types to be refined, enabling the types of stored values to depend on the keys used to access them. For example, the polymorphic refinement type $(i: \alpha, \beta)$ Map.t corresponds to a hash-map where each key i has the (refined) type α , and is mapped to a value of type β , which can depend on the key i . For example, $(i: \text{int}, \{\nu: \text{int} \mid i < \nu\})$ Map.t specifies a hash-map where each integer key is mapped to a value greater than the key. Suppose that the edge-adjacency relation of a graph is represented as a hash-map that maps each node’s integer identifier to its list of successors. Then $(i: \text{int}, \{\nu: \text{int} \mid i < \nu\})$ Map.t specifies that the graph is *acyclic*, as each successor is greater than its source.

These two mechanisms allow us to harness the complementary strengths of types and SMT solvers to automatically verify data structures. Types tackle quantifiers by using subtyping to reduce global quantified relationships into local implication checks over simple quantifier-free predicates; SMT solvers make light work of the task of discharging these implication checks. Furthermore, we can use the abstract interpretation framework of liquid types [18] to automatically *infer* refinements from a set of simple logical qualifiers, thereby eliminating the prohibitive cost of writing type annotations for all functions and polymorphic instantiations.

We have implemented recursive and polymorphic refinements in DSOLVE, a tool that infers liquid types for OCAML programs. We describe experiments using DSOLVE to verify a variety of complex data structure invariants including sortedness, balancedness, binary-search-ordering, variable ordering, set-implementation, heap-implementation, and acyclicity on several benchmarks including textbook data structures such as sorted lists, union-find, splay heaps, AVL trees, red-black trees, and heavily-used libraries implementing stable sort, heaps, associative maps, extensible vectors, and binary decision diagrams. Previously, the verification of such properties required significant manual annotations in the form of loop invariants, pre- and post-conditions, and proof scripts totaling several times the code size. In contrast, DSOLVE is able to verify complex properties using a handful of simple qualifiers amounting to 4% of the code size.

2. Overview

We begin with an overview of our type-based approach for verifying complex data structures. We start with a quick review of simple refinement and liquid types. Next, we describe *recursive* and *polymorphic* refinements, and through examples, illustrate how they can be used to verify data structure invariants.

Refinement Types. Our system is built on the idea of *refining* ML types with predicates over program values that specify additional constraints satisfied by values of the type [9]. Base values, for example, those of type integer (denoted `int`), can be described as $\{\nu: \text{int} \mid e\}$ where ν is a special *value variable* not appearing in the program, and e is a boolean-valued expression constraining the value variable called the *refinement predicate*. Intuitively, the base refinement predicate specifies the set of values c of the base type B such that the predicate $e[c/\nu]$ evaluates to true. For example, $\{\nu: \text{int} \mid \nu \leq n\}$ specifies the set of integers whose value is less than or equal to the value of the variable n . We use the base refinements to build up *dependent function types*, written $x: T_1 \rightarrow T_2$. Here, T_1 is the domain type of the function, and the formal parameter x may appear in the base refinements of the range type T_2 . For example, $x: \text{int} \rightarrow \{\nu: \text{int} \mid x \leq \nu\}$ is the type of a function that takes an input integer and returns an integer greater than the input. Thus, the type `int` abbreviates $\{\nu: \text{int} \mid \top\}$, where \top and \perp abbreviate *true* and *false*.

Liquid Types. A *logical qualifier* is a boolean-valued expression (i.e., predicate) over the program variables, the special value variable ν which is distinct from the program variables, and the special placeholder variable \star that can be instantiated with program variables. We say that a qualifier q *matches* the qualifier q' if replacing some subset of the free variables in q with \star yields q' . For example, the qualifier $i \leq \nu$ matches the qualifier $\star \leq \nu$. We write \mathbb{Q}^* for the set of all qualifiers *not containing* \star that match some qualifier in \mathbb{Q} . In the rest of this section, let \mathbb{Q} be the qualifiers $\{0 \leq \nu, \star \leq \nu\}$. A *liquid type over* \mathbb{Q} is a dependent type where the refinement predicates are conjunctions of qualifiers from \mathbb{Q}^* . We write *liquid type* when \mathbb{Q} is clear from the context. We can automatically *infer* refinement types by requiring that certain expressions like recursive functions have liquid types [18].

Safety Verification. Refinement types can be used to statically prove safety properties by encoding appropriate preconditions into the types of primitive operations. For example, to prove that no divide-by-zero or assertion failures occur at run-time, we can type check the program using the types $\text{int} \rightarrow \{\nu: \text{int} \mid \nu \neq 0\} \rightarrow \text{int}$ and $\{\nu: \text{bool} \mid \nu\} \rightarrow \text{unit}$ for division and `assert` respectively.

2.1 Recursive Refinements

The first building block for data structures is recursion, formalized using recursive types, which are used to fashion structures like lists and trees. The key idea behind recursive refinements is that recursive types can be refined using a matrix of predicates, where each predicate applies to a particular element of the recursive type. For ease of exposition, we consider recursive types whose body is a sum-of-products. Each product can be refined using a *product refinement*, which is a vector of predicates where the j^{th} predicate refines the j^{th} element of the product. Each sum-of-products (and hence, the entire recursive type), can be refined with a *recursive refinement*, which is a vector of product refinements, where the i^{th} product refinement refines the i^{th} element of the sum.

Uniform Refinements. The ML type for integer lists is:

$$\mu t. \text{Nil} + \text{Cons}\langle x_1: \text{int}, x_2: t \rangle$$

a recursive sum-of-products which we abbreviate to `int list`. We specify a list of integers each of which satisfies a predicate p as $\langle\langle\langle; \langle p; \top \rangle \rangle\rangle\rangle \text{ int list}$. For example,

$\langle\langle\langle; \langle i \leq \nu; \top \rangle \rangle\rangle\rangle \text{ int list}$ specifies lists of integers greater than some program variable i . This representation simplifies specification (resp. inference) by reducing it to specifying (resp. inferring) which logical qualifiers apply at each position of the refinement matrix. In the sequel, for any expression e and relation \bowtie we define the abbreviation ρ_{\bowtie}^e as:

$$\rho_{\bowtie}^e \doteq \langle\langle; \langle e \bowtie \nu; \top \rangle \rangle \quad (1)$$

To see how such uniform refinements can be used for verification, consider the program in Figure 1. The function `range` takes two integers, i and j , and returns the list of integers i, \dots, j . The function `harmonic` takes an integer n and returns the n^{th} (scaled) harmonic number. To do so, `harmonic` first calls `range` to get the list of denominators $1, \dots, n$, and then calls `List.fold_left` with an accumulator to compute the harmonic number. To prove that the divisions inside `harmonic` are safe, we need to know that all the integers in the list `is` are non-zero. Using \mathbb{Q} , our system infers:

$$\text{range} :: i: \text{int} \rightarrow j: \text{int} \rightarrow (\rho_{\leq}^i) \text{ int list}$$

By substituting the actuals for the formals in the inferred type for `range`, our system infers that the variable `is` has the type: $(\rho_{\leq}^1) \text{ int list}$. As the polymorphic ML type for `List.fold_left` is: $\forall \alpha, \beta. (\alpha \rightarrow \beta \rightarrow \alpha) \rightarrow \alpha \rightarrow \beta \text{ list} \rightarrow \alpha$ our system infers that at the application to `List.fold_left` inside `harmonic`, α and β are respectively instantiated with `int` and $\{\nu: \text{int} \mid 1 \leq \nu\}$, and hence, that the accumulator has the type:

$$s: \text{int} \rightarrow k: \{\nu: \text{int} \mid 1 \leq \nu\} \rightarrow \text{int}$$

As k is at least 1, our system successfully typechecks the application of the division operator `/`, proving the program division safe.

Nested Refinements. The function `range` returns an *increasing* sequence of integers. Recursive refinements can capture this invariant by applying the recursive refinement to the μ -bound variables *inside* the recursive type. For each type τ define:

$$\tau \text{ list}_{\leq} \doteq \mu t. \text{Nil} + \text{Cons}\langle x_1: \tau, x_2: (\rho_{\leq}^{x_1}) t \rangle \quad (2)$$

Thus, a list of increasing integers is `int list≤`. This succinctly captures the fact that the list is increasing, since the result of “unfolding” the type by substituting each occurrence of t with the entire recursively refined type is:

$$\text{Nil} + \text{Cons}\langle x'_1: \text{int}, x'_2: (\rho_{\leq}^{x'_1}) \text{ int list}_{\leq} \rangle$$

where x'_1, x'_2 are fresh names introduced for the top-level “head” and “tail”. Intuitively, this unfolded sum type corresponds to a value that is either the empty list or a list whose head, x'_1 , is smaller than each integer in the tail and whose tail is an increasing sequence.

For inference, we need only find which qualifiers flow into the recursive refinement matrices. Using \mathbb{Q} , our system infers that `range` returns an increasing list of integers no less than i :

$$\text{range} :: i: \text{int} \rightarrow j: \text{int} \rightarrow (\rho_{\leq}^i) \text{ int list}_{\leq}$$

As another example, consider the insertion sort function from Figure 2. In a manner similar to the analysis for `range`, using just \mathbb{Q} , and no other annotations, our system infers that `insert` has the type $x: \alpha \rightarrow \text{ys}: \alpha \text{ list}_{\leq} \rightarrow \alpha \text{ list}_{\leq}$, and hence that program correctly sorts lists or, formally, the system infers that `insertsort` has the type $\text{xs}: \alpha \text{ list} \rightarrow \alpha \text{ list}_{\leq}$.

Structure Refinements. Suppose we wish to check that the output list has the same *set of elements* as the input list. We first specify what we mean by “the set of elements” of the list using a *measure*: an inductive, terminating function that we can soundly use in refinements. The following specifies the set of elements in a list:

$$\begin{aligned} \text{measure elts} &= [] && \rightarrow \text{empty} \\ &| x: \text{xs} && \rightarrow \text{union}(\text{single } x) (\text{elts } \text{xs}) \end{aligned}$$

where `empty`, `union` and `single` are primitive constants corresponding to the respective set values and operations. Using just the measure specification and the SMT solver’s decidable theory of sets our system infers that `insertsort` has the type:

$$xs : \alpha \text{ list} \rightarrow \{\nu : \alpha \text{ list}_{\leq} \mid \text{elts } \nu = \text{elts } xs\}$$

i.e., the output list is sorted and has the same elements as the input list. In Section 6 we show how properties like balancedness can be verified by combining measures (to specify heights) and recursive refinements (to specify balancedness at each level).

2.2 Polymorphic Refinements

The second building block for data structures are finite maps. Examples of maps include arrays, vectors, hash tables *etc.* The classical way to model maps is using the array axioms [6], and by using using algorithmically problematic, universally quantified formulas to capture properties over *all* key-value bindings. Polymorphic refinements allow the representation of these universal invariants within the type system without explicit quantification and provide a strategy for generalizing and instantiating quantifiers in order to verify and infer universal invariants of maps. To achieve this, we extend the well-understood notion of parametric polymorphism to include *refined* polytype variables and schemas. Using polymorphic refinements, we can give a polymorphic map the type $(i : \alpha, \beta) \text{ Map.t}$. Intuitively, this type describes a map, where *every* key i of type α is mapped to a value of type β and β can refer to i . For example, if we instantiate α and β with `int` and $\{\nu : \text{int} \mid 1 < \nu \wedge i - 1 < \nu\}$, the resulting type describes a map from integer keys i to integer values greater than 1 and $i - 1$.

Memoization. Consider the memoized fibonacci function `fib` in Figure 3. The example is shown in the SSA-converted style of Appel [2], with `t0` being the input name of the memo table, and `t1`, `t2` the names after updates. To verify that `fib` always returns a value greater than 1 and greater than (the argument) $i - 1$, we require the universally quantified invariant that *every* key j in the memo table `t0` is mapped to a value greater than 1 and greater than $j - 1$. Using the qualifiers $\{1 \leq \nu, \star - 1 \leq \nu\}$, our system infers that the polytype variables α and β can be instantiated as described above, and so the map `t0` has type $(i : \text{int}, \{\nu : \text{int} \mid 1 \leq \nu \wedge i - 1 \leq \nu\}) \text{ Map.t}$, i.e., every integer key i is mapped to a value greater than 1 and $i - 1$, using which, the system infers that `fib` has type $i : \text{int} \rightarrow \{\nu : \text{int} \mid 1 \leq \nu \wedge i - 1 \leq \nu\}$.

Directed Graphs. Consider the function `build_dag` from Figure 4, which represents a directed graph with the pair (n, g) . n is the number of nodes of the graph, and g is a map that encodes the link structure by mapping each node, an integer less than n , to the list of its directed successors, each of which is also an integer less than n . In each iteration, the function `build_dag` randomly chooses a node in the graph, looks up the map to find its successors, creates a new node $n+1$ and adds $n+1$ to the successors of the node in the graph. As each node’s successors are greater than the node, there can be no cycles in the graph. Using just the qualifiers $\{\nu \leq \star, \star < \nu\}$ our system infers that the function `build_dag` inductively constructs a directed acyclic graph. Formally, the system infers that `g1` has type

$$DAG \doteq (i : \text{int}, (\langle \langle \rangle; \langle i < \nu; \top \rangle \rangle) \text{ int list}) \text{ Map.t} \quad (3)$$

which specifies that each node i has successors that are *strictly greater* than i . Thus, by combining recursive and polymorphic refinements, our system is able to infer complex shape invariants about linked structures from simple quantifier-free predicates.

3. Language

We begin by reviewing the core language ML_{ref} from [18], by presenting its syntax and static semantics. ML_{ref} has a strict, call-by-

e	$::=$	x c $\lambda x.e$ $e e$ $\text{if } e \text{ then } e \text{ else } e$ $\text{let } x = e \text{ in } e$ $[\Lambda \alpha]e$ $[\tau]e$	<i>Expressions:</i> variable constant abstraction application if-then-else let-binding type-abstraction type-instantiation
Q	$::=$	\top q $Q \wedge Q$	<i>Liquid Refinements</i> true qualifier in \mathbb{Q}^* conjunction
B	$::=$	int bool α	<i>Base:</i> integers booleans type variable
$A(\mathbb{B})$	$::=$	B $x : \mathbb{T}(\mathbb{B}) \rightarrow \mathbb{T}(\mathbb{B})$	<i>Unrefined Skeletons:</i> base function
$\mathbb{T}(\mathbb{B})$	$::=$	$\{\nu : A(\mathbb{B}) \mid \mathbb{B}\}$	<i>Refined Skeletons:</i> refined type
$\mathbb{S}(\mathbb{B})$	$::=$	$\mathbb{T}(\mathbb{B})$ $\forall \alpha. \mathbb{S}(\mathbb{B})$	<i>Type Schema Skeletons:</i> monotype polytype
τ, σ	$::=$	$\mathbb{T}(\mathbb{T}), \mathbb{S}(\mathbb{T})$	<i>Types, Schemas</i>
T, S	$::=$	$\mathbb{T}(E), \mathbb{S}(E)$	<i>Depend. Types, Schemas</i>
\hat{T}, \hat{S}	$::=$	$\mathbb{T}(Q), \mathbb{S}(Q)$	<i>Liquid Types, Schemas</i>

Figure 5. ML_{ref} : Syntax

value semantics, formalized using a standard small-step operational semantics [1]. In Section 4 (resp. Section 5), we extend ML_{ref} with recursive refinements (resp. polymorphic refinements).

Expressions. The expressions of ML_{ref} , summarized in Figure 5, include variables, primitive constants, λ -abstractions and function application. In addition, ML_{ref} has `let`-bindings and recursive function definitions using the constant `fix`. Our type system conservatively extends ML-style parametric polymorphism; we assume that the ML type inference algorithm automatically places appropriate type generalization and instantiation annotations into the source expression. Finally, we assume that via α -renaming, each variable is bound at most once in an environment.

Types and Schemas. ML_{ref} has a system of base types, function types, and ML-style parametric polymorphism using type variables α and schemas where the type variables are quantified at the outermost level. We organize types into *unrefined types* which have no top-level refinement predicates, but which may be composed of types which are themselves refined, and *refined types* which have a top-level refinement predicate. An *ML type (schema)* is a type (schema) where all the refinement predicates are \top . A *Liquid type (schema)* is a type (schema) where all the refinement predicates are conjunctions of qualifiers from \mathbb{Q}^* . We write τ and σ for ML types and schemas, T and S for refinement types and schemas, and \hat{T} and \hat{S} for liquid types and schemas. We write τ to abbreviate $\{\nu : \tau \mid \top\}$, and $\bar{\tau}$ to abbreviate $\{\nu : \tau \mid \perp\}$. When τ is clear from context, we write $\{e\}$ to abbreviate $\{\nu : \tau \mid e\}$.

Constants. The basic units of computation in ML_{ref} are primitive constants, which are given refinement types that precisely capture their semantics. Primitive constants include basic values, like integers and booleans, as well as primitive functions that define basic operations. The input types for primitive functions describe the values for which the function is defined, and the output types describe

```

let rec range i j =
  if i > j then
    []
  else
    let is = range (i+1) j in
    i::is

let harmonic n =
  let is = range 1 n in
  List.fold_left
    (fun s k -> s + 10000/k)
    0 is

let rec insert x ys =
  match ys with
  | [] -> [x]
  | y::ys' ->
    if x < y then x::y::ys'
    else y::(insert x ys')

let rec insertsort xs =
  match xs with
  | [] -> []
  | x::xs' ->
    insert x (insertsort xs')

```

Figure 1. Divide-by-zero

```

let rec f t0 n =
  let rec f t0 n =
    if mem t0 n then
      (t0, get t0 n)
    else if n <= 2 then
      (t0, 1)
    else
      let (t1,r1) = f t0 (n-1) in
      let (t2,r2) = f t1 (n-2) in
      let r = r1 + r2 in
      (set t2 n r, r) in
  snd (f (create 17) i)

```

Figure 2. Insertion Sort

```

let rec build_dag (n, g) =
  let node = random () in
  if (0 > node || node >= n) then
    (n, g)
  else
    let succs = get g node in
    let succs' = (n+1)::succs in
    let g' = set g node succs' in
    build_dag (n+1, g')

let g0 = set (create 17) 0 []
let (_,g1) = build_dag (1, g0)

```

Figure 3. Memoization

```

let rec build_dag (n, g) =
  let node = random () in
  if (0 > node || node >= n) then
    (n, g)
  else
    let succs = get g node in
    let succs' = (n+1)::succs in
    let g' = set g node succs' in
    build_dag (n+1, g')

let g0 = set (create 17) 0 []
let (_,g1) = build_dag (1, g0)

```

Figure 4. Acyclic Graph

the values returned by the function. To allow recursive functions ML_{ref} includes a constant `fix` of type $\forall \alpha. (\alpha \rightarrow \alpha) \rightarrow \alpha$.

Next, we give an overview of our static type system, by describing environments and summarizing the different kinds of judgments.

Environments and Shapes. A *type environment* Γ is a sequence of *type bindings* of the form $x:S$ and *guard predicates* e . The guard predicates are used to capture constraints about “path” information corresponding to the branches followed in if-then-else expressions. The *shape* of a refinement type (schema) S , written as $\text{Shape}(S)$, is the ML type (schema) obtained by replacing all the refinement predicates with \top (i.e., “erasing” the refinement predicates). The shape of an environment is the ML type environment obtained by applying Shape to each type binding and removing the guards.

Judgments. Our system has four kinds of judgments that relate environments, expressions, recursive refinements and types. Well-formedness judgments ($\Gamma \vdash S$) state that a type (schema) S is *well-formed* under environment Γ . Intuitively, the judgment holds if the refinement predicates are boolean expressions in Γ . Subtyping judgments ($\Gamma \vdash S_1 <: S_2$) state that the type (schema) S_1 is a *subtype* of the type (schema) S_2 under environment Γ . Intuitively, the judgments state that, under the value-binding and guard constraints imposed by Γ , the set of values described by S_1 is contained in the set of values described by S_2 . Typing judgments ($\Gamma \vdash_Q e : S$) state that, using the logical qualifiers \mathbb{Q} , the expression e has the type schema S under environment Γ . Intuitively, the judgments state that, under the value-binding and guard constraints imposed by Γ , the expression e will evaluate to a value described by the type S .

Decidable Subtyping and Liquid Type Inference. In order to determine whether one type is a subtype of another, our system uses the subtyping rules (Figure 6) to generate a set of implication checks over refinement predicates. To ensure that these implication checks are decidable, we *embed* the implication checks into a decidable logic of equality, uninterpreted functions, and linear arithmetic (EUF) that can be decided by an SMT solver [6]. As the types of branches, functions, and polymorphic instantiations are liquid, we can automatically infer liquid types for programs using abstract interpretation [18].

Soundness. We have proven the soundness of the type system by showing that if an expression is well-typed then we are guaranteed that evaluation does not get “stuck”, i.e., at run-time, every primitive operation receives valid inputs. We defer the details about the judgments, the proof of soundness, and type inference to [1].

4. Recursive Refinements

We now describe how the core language is extended with recursively refined types which capture invariants of recursive types. First, we describe how we extend the syntax of expressions and types, and correspondingly extend the dynamic semantics. Second, we describe measures, a special class of functions that are syntac-

Well-Formed Types

$\Gamma \vdash S$

$$\frac{\text{Shape}(T) = T}{\Gamma \vdash T} \text{ [WF-REFLEX]}$$

$$\frac{\Gamma \vdash T \quad \Gamma; \nu : \text{Shape}(T) \vdash e : \text{bool}}{\Gamma \vdash \{\nu : T \mid e\}} \text{ [WF-REFINE]}$$

$$\frac{\Gamma \vdash T \quad \Gamma; x : \text{Shape}(T) \vdash T'}{\Gamma \vdash x : T \rightarrow T'} \text{ [WF-FUN]}$$

Decidable Subtyping

$\Gamma \vdash S_1 <: S_2$

$$\frac{}{\Gamma \vdash T <: T} \text{ [<:-REFLEX]}$$

$$\frac{\Gamma \vdash T <: T' \quad \text{Valid}([\Gamma] \wedge [e] \Rightarrow [e'])}{\Gamma \vdash \{\nu : T \mid e\} <: \{\nu : T' \mid e'\}} \text{ [<:-REFINE]}$$

$$\frac{\Gamma \vdash T_2 <: T_1 \quad \Gamma; x_2 : T_2 \vdash T'_1[x_2/x_1] <: T'_2}{\Gamma \vdash x_1 : T_1 \rightarrow T'_1 <: x_2 : T_2 \rightarrow T'_2} \text{ [<:-FUN]}$$

Liquid Type Checking

$\Gamma \vdash_Q e : S$

$$\frac{\Gamma \vdash_Q e : S \quad \Gamma \vdash S <: S' \quad \Gamma \vdash_Q S'}{\Gamma \vdash_Q e : S'} \text{ [L-SUB]}$$

$$\frac{\Gamma(x) = \{\nu : T \mid e\}}{\Gamma \vdash_Q x : \{\nu : T \mid e \wedge \nu = x\}} \text{ [L-VAR]} \quad \frac{}{\Gamma \vdash_Q c : \text{ty}(c)} \text{ [L-CONST]}$$

$$\frac{\Gamma \vdash x : \hat{T}_x \rightarrow \hat{T} \quad \Gamma; x : \hat{T}_x \vdash_Q e : \hat{T}}{\Gamma \vdash_Q (\lambda x. e) : x : \hat{T}_x \rightarrow \hat{T}} \text{ [L-FUN]}$$

$$\frac{\Gamma \vdash_Q e_1 : x : T \rightarrow T' \quad \Gamma \vdash_Q e_2 : T}{\Gamma \vdash_Q e_1 e_2 : T'[e_2/x]} \text{ [L-APP]}$$

$$\frac{\Gamma \vdash \hat{T} \quad \Gamma \vdash_Q e_1 : \text{bool} \quad \Gamma; e_1 \vdash_Q e_2 : \hat{T} \quad \Gamma; \neg e_1 \vdash_Q e_3 : \hat{T}}{\Gamma \vdash_Q \text{if } e_1 \text{ then } e_2 \text{ else } e_3 : \hat{T}} \text{ [L-IF]}$$

$$\frac{\Gamma \vdash \hat{T} \quad \Gamma \vdash_Q e_1 : S_1 \quad \Gamma; x : S_1 \vdash_Q e_2 : \hat{T}}{\Gamma \vdash_Q \text{let } x = e_1 \text{ in } e_2 : \hat{T}} \text{ [L-LET]}$$

$$\frac{\Gamma \vdash_Q e : S \quad \alpha \notin \Gamma}{\Gamma \vdash_Q [\Lambda \alpha] e : \forall \alpha. S} \text{ [L-GEN]}$$

$$\frac{\Gamma \vdash T \quad \text{Shape}(T) = \tau \quad \Gamma \vdash_Q e : \forall \alpha. S}{\Gamma \vdash_Q [\tau] e : S[T/\alpha]} \text{ [L-INST]}$$

Figure 6. Liquid Type Checking Rules

tically guaranteed to terminate, thereby allowing us to use them to refine recursive types with predicates that capture structural properties of recursive values. Finally, we extend the static type system

e ::= ... $\langle e \rangle$ $C(e)$ $\text{match } e \text{ with } _{i} C_i \langle x_i \rangle \mapsto e_i$ $\text{unfold } e$ $\text{fold } e$	<i>Expressions:</i> tuple constructor match-with unfold fold
ε ::= $m \mid c \mid x \mid \varepsilon \varepsilon$	<i>M-Expressions</i>
M ::= $(m, \langle C_i \langle x_i \rangle \mapsto \varepsilon_i \rangle, \tau, \mu t. \Sigma_i C_i \langle x_i : \tau_i \rangle)$	<i>Measures</i>
$\rho(\mathbb{B})$::= $\langle \langle \mathbb{B} \rangle^* \rangle$	<i>Recursive Refinement</i>
$\mathbb{A}(\mathbb{B})$::= ... $\langle x : \mathbb{T}(\mathbb{B}) \rangle$ $\Sigma_i C_i \langle x : \mathbb{T}(\mathbb{B}) \rangle$ $(\rho(\mathbb{B})) t$ $(\rho(\mathbb{B})) \mu t. \Sigma_i C_i \langle x : \mathbb{T}(\mathbb{B}) \rangle$	<i>Unrefined Skeletons:</i> product sum recursive type variable recursive type

Figure 7. Recursive Refinements: Syntax

by formalizing the derivation rules that deal with recursive values, and illustrate how the rules are applied to check expressions.

4.1 Syntax and Dynamic Semantics

Figure 7 describes how the expressions and types of ML_{ref} are extended to include recursively defined values. The language of expressions includes tuples, constructors, folds, unfolds, and pattern-match expressions. The language of types is extended with product types, tagged sum types, and a system of *iso-recursive types*. We assume that appropriate `fold` and `unfold` annotations are automatically placed in the source at the standard construction and matching sites respectively [16]. We assume that different types use disjoint constructors, and that in pattern-match expressions there is exactly one match-binding for each constructor of the appropriate type. The run-time values are extended with tuples, tags and explicit fold/unfold values in the standard manner [16].

Notation. We write $\langle Z \rangle$ for a sequence of values of the kind Z . We write $(Z; \langle Z \rangle)$ for a sequence of values whose first element is Z , and the remaining elements are $\langle Z \rangle$. We write $\langle \rangle$ for the empty sequence. As for functions, we write dependent tuple types using a sequence of name-to-type bindings, and allow refinements for “later” elements in the tuple to refer to previous elements. For example, $\langle x_1 : \text{int}; x_2 : \{\nu : \text{int} \mid x_1 \leq \nu \} \rangle$ is the type of pairs of integers where the second element is greater than the first.

Measures. Many important attributes of recursive structures, e.g., the length or set of elements of a list or the height of a tree, are inductive and hence most naturally specified using recursive functions. However, the use of arbitrary, potentially non-terminating functions in refinement predicates leads to unsoundness. This makes it difficult to use refinements to capture invariants over inductive attributes. To solve this problem, we represent such attributes using *measures*, a syntactic class of first order functions from the recursive type to some other type which are guaranteed to terminate, thereby allowing us to soundly use them inside refinements. Figure 7 shows the syntax of measure functions. A *measure name* m is a special variable drawn from a set of measure names. A *measure expression* ε is an expression drawn from a restricted language of variables, constants, measure names and application. A *measure for a recursive (ML) type* $\mu t. \Sigma_i C_i \langle x_i : \tau_i \rangle$ of type τ is a quadruple: $(m, \langle C_i \langle x_i \rangle \mapsto \varepsilon_i \rangle, \tau, \mu t. \Sigma_i C_i \langle x_i : \tau_i \rangle)$. A *measure specification* is an ordered sequence of measures. As measures are defined by structural induction, they are well-founded and hence total. Figure 8 shows the rules used to check that a measure specification is well-formed. We assume that the measure specification $\langle M \rangle$ is well-formed, i.e., $\emptyset \vdash \langle M \rangle$.

Measure Well-formedness

$$\frac{\forall i : \Gamma; \langle x_i : \tau_i \rangle \vdash \varepsilon_i : \tau \quad \Gamma; m : \mu t. \Sigma_i C_i \langle x_i : \tau_i \rangle \rightarrow \tau \vdash \langle M \rangle}{\Gamma \vdash (m, \langle C_i \langle x_i \rangle \mapsto \varepsilon_i \rangle, \tau, \mu t. \Sigma_i C_i \langle x_i : \tau_i \rangle); \langle M \rangle} \text{ [WF-M]}$$

ρ -Application

$$\frac{(\rho) T \triangleright T'}{\text{fresh } x' (\langle e \rangle [x'/x]) \langle x : T[x'/x] \rangle \triangleright \langle x' : T' \rangle} \text{ [}\triangleright\text{-PROD]}$$

$$\frac{\forall i : (\rho_i) \langle x_i : T_i \rangle \triangleright \langle x'_i : T'_i \rangle}{(\rho) \Sigma_i C_i \langle x_i : T_i \rangle \triangleright \Sigma_i C_i \langle x'_i : T'_i \rangle} \text{ [}\triangleright\text{-SUM]}$$

Well-Formed Types

$$\frac{\Gamma \vdash T \quad \Gamma; x : \text{Shape}(T) \vdash \langle x : T \rangle}{\Gamma \vdash (x : T, \langle x : T \rangle)} \text{ [WF-PROD]}$$

$$\frac{\forall i : \Gamma \vdash \langle x_i : T_i \rangle}{\Gamma \vdash \Sigma_i C_i \langle x_i : T_i \rangle} \text{ [WF-SUM]}$$

$$\frac{(\rho) T \triangleright T' \quad \Gamma \vdash T' [\text{Shape}(\mu t. T)/t]}{\Gamma \vdash (\rho) \mu t. T} \text{ [WF-REC]}$$

Decidable Subtyping

$$\frac{\Gamma \vdash T <: T' \quad \Gamma; x : T \vdash \langle x : T \rangle <: \langle x' : T' [x/x'] \rangle}{\Gamma \vdash (x : T, \langle x : T \rangle) <: (x' : T', \langle x' : T' \rangle)} \text{ [}<:\text{-PROD]}$$

$$\frac{\forall i : \Gamma \vdash \langle x_i : T_i \rangle <: \langle x'_i : T'_i \rangle}{\Gamma \vdash \Sigma_i C_i \langle x_i : T_i \rangle <: \Sigma_i C_i \langle x'_i : T'_i \rangle} \text{ [}<:\text{-SUM]}$$

$$\frac{(\rho_1) T_1 \triangleright T'_1 \quad (\rho_2) T_2 \triangleright T'_2 \quad \tau = \text{Shape}(\mu t. T_1) = \text{Shape}(\mu t. T_2)}{\Gamma \vdash T'_1 [\tau/t] <: T'_2 [\tau/t]} \text{ [}<:\text{-REC]}$$

Liquid Type Checking

$$\frac{\Gamma \vdash (\hat{\rho}) \mu t. \hat{T} \quad (\rho) \hat{T} \triangleright \hat{T}'}{\Gamma \vdash_Q e : \{\nu : \hat{T}' [(\hat{\rho}) \mu t. \hat{T}/t] \mid e'\}} \text{ [L-FOLD-M]}$$

$$\frac{(\rho) T \triangleright T' \quad \Gamma \vdash e : \{\nu : (\rho) \mu t. T \mid e'\}}{\Gamma \vdash \text{unfold } e : \{\nu : T' [(\rho) \mu t. T/t] \mid e'\}} \text{ [L-UNFOLD-M]}$$

$$\frac{\Gamma \vdash_Q \langle e \rangle : \langle x : T \rangle}{\Gamma \vdash_Q C_j \langle e \rangle : \{\nu : C_j \langle x : T \rangle + \Sigma_{i \neq j} C_i \langle x_i : \bar{\tau}_i \rangle \mid \wedge_{\mathbf{m}}(\nu) = \varepsilon_j(\langle e \rangle)\}} \text{ [L-SUM-M]}$$

$$\frac{\Gamma \vdash e : \Sigma_i C_i \langle x_i : T_i \rangle \quad \Gamma \vdash T}{\forall i \Gamma; \langle x_i : T_i \rangle; \wedge_{\mathbf{m}}(e) = \varepsilon_i(x_i) \vdash e_i : \hat{T}} \text{ [L-MATCH-M]}$$

Figure 8. Recursive Refinements: Static Semantics

4.2 Static Semantics

The derivation rules pertaining to recursively refined types, including the rules for product, sum, and recursive types, as well as construction, match-with, fold and unfold expressions are shown in Figure 8. The fold and unfold rules use a judgment called ρ -application, written $(\rho) T \triangleright T'$. Intuitively, this judgment states that when a recursive refinement ρ is applied to a type T , the result is a sum type T' with refinements from ρ . Next, we describe each judgment and the rules relevant to it.

ρ -Application and Unfolding. Formally, a recursive type $(\rho) \mu t. T$ is unfolded in two steps. First, we apply the recursive refinement ρ to the *body* of the recursive type T to get the result T' (written $(\rho) T \triangleright T'$). Second, in the result of the application T' , we

replace the μ -bound recursive type variable t with the entire original recursive type $(\rho) \mu t.T$, and *normalize* by replacing adjacent refinements $(\rho)(\rho')$ with a new refinement ρ'' which contains the conjunctions of corresponding predicates from ρ and ρ' .

Example. Consider the type which describes increasing lists of integers greater than some x'_1 :

$$(\rho_{\leq}^{x'_1}) \mu t. \text{Nil} + \text{Cons}\langle x_1 : \text{int}, x_2 : (\rho_{\leq}^{x'_1}) t \rangle$$

To unfold this type, we first apply the recursive refinement $\rho_{\leq}^{x'_1}$ to the recursive type's body, $\text{Nil} + \text{Cons}\langle x_1 : \text{int}, x_2 : (\rho_{\leq}^{x'_1}) t \rangle$. To apply the recursive refinement to the above sum type, we use rule $[\triangleright\text{-SUM}]$ to apply the product refinements $\langle \rangle$ and $\langle x'_1 \leq \nu; \top \rangle$ of $\rho_{\leq}^{x'_1}$ to the products corresponding to the parameters of the Nil and Cons constructors, respectively. To apply the refinements to each product, we use the rule $[\triangleright\text{-PROD}]$ to obtain the result:

$$\text{Nil} + \text{Cons}\langle x'_1 : \{ \nu : \text{int} \mid x'_1 \leq \nu \}, x'_2 : (\rho_{\leq}^{x'_1}) t \rangle \quad (4)$$

Notice that the result is a sum type with fresh names for the “head” and “tail” of the unfolded list. Observe that this renaming allows us to soundly use the head's value to refine the tail, via a recursive refinement stipulating all elements in the tail are greater than the head, x'_1 . To complete the unfolding, we replace t with the entire recursive type and normalize to get:

$$\text{Nil} + \text{Cons}\langle x'_1 : \{ \nu : \text{int} \mid x'_1 \leq \nu \}, x'_2 : (\rho) \text{int list} \rangle$$

where $\rho \doteq \langle \langle \rangle; \langle x'_1 \leq \nu \wedge x'_1 \leq \nu; \top \rangle \rangle$. Intuitively, the result of the unfolding is a type that specifies an empty list, or a non-empty list with a head greater than x'_1 and an increasing tail whose elements are greater than x'_1 and x'_1 .

Well-formedness. Rule $[\text{WF-REC}]$ checks if a recursive type $(\rho) \mu t.T$ is well-formed in an environment Γ . First, the rule applies the refinement ρ to T , the body of the recursive type, to obtain T' . Next, the rule replaces the μ -bound variable t in T' with the *shape* of the recursive type and checks the well-formedness of the result of the substitution.

Example. When $\rho_{\top} \doteq \langle \langle \rangle; \langle \top; \top \rangle \rangle$, the check

$$\emptyset \vdash (\rho_{\top}) \mu t. \text{Nil} + \text{Cons}\langle x_1 : \text{int}, x_2 : (\rho_{\leq}^{x_1}) t \rangle$$

is reduced to checking, using $[\text{WF-REFINE}]$ that in the environment where x'_1 (the fresh name given to the unfolded list's “head”) has type int , the refinement $\{ \nu : \text{int} \mid x'_1 \leq \nu \}$ (applied to the elements of the unfolded list's “tail”) is well-formed.

Subtyping. Rule $[\text{<:-PROD}]$ (resp. $[\text{<:-SUM}]$) determines whether two products (resp. sums) satisfy the subtyping relation by checking subtyping between corresponding elements.

Example. When $\Gamma \doteq x'_1 : \text{int}$ the check

$$\Gamma \vdash \langle y_1 : \{ x'_1 < \nu \}, y_2 : \text{int list} \rangle <: \langle z_1 : \{ x'_1 \neq \nu \}, z_2 : \text{int list} \rangle \quad (5)$$

is reduced by $[\text{<:-PROD}]$ to:

$$\Gamma \vdash \{ \nu : \text{int} \mid x'_1 < \nu \} <: \{ \nu : \text{int} \mid x'_1 \neq \nu \}$$

$$\Gamma; y_1 : \text{int} \vdash \text{int list} <: \text{int list}$$

$[\text{<:-REFLEX}]$ ensures the latter. $[\text{<:-REFINE}]$ reduces the former to checking the validity of $(x'_1 < \nu) \Rightarrow x'_1 \neq \nu$ in EUFA.

Rule $[\text{<:-REC}]$ determines whether the subtyping relation holds between two recursively refined types. The rule first applies the outer refinements to the bodies of the recursive types, then substitutes the μ -bound variable with the *shape* (as for well-formedness), and then checks that the resulting sums are subtypes.

Example. Consider the subtyping check

$$x'_1 : \text{int} \vdash (\rho_{<}^{x'_1}) \text{int list} <: (\rho_{\neq}^{x'_1}) \text{int list} \quad (6)$$

that is, the list of integers greater than x'_1 is a subtype of the list of integers distinct from x'_1 . Rule $[\text{<:-REC}]$ applies the refinements to the bodies of the recursive types and then substitutes the shapes, reducing the above (after using $[\text{<:-SUM}]$, $[\text{<:-PROD}]$ and $[\text{<:-REFLEX}]$) to (5). Finally, the rule $[\text{<:-REC}]$ yields the judgment

$$\emptyset \vdash \text{int list}_{<} <: \text{int list}_{\neq} \quad (7)$$

$$\text{int list}_{<} \doteq (\rho_{\top}) \mu t. \text{Nil} + \text{Cons}\langle x_1 : \text{int}, x_2 : (\rho_{<}^{x_1}) t \rangle$$

$$\text{int list}_{\neq} \doteq (\rho_{\top}) \mu t. \text{Nil} + \text{Cons}\langle x_1 : \text{int}, x_2 : (\rho_{\neq}^{x_1}) t \rangle$$

that is, that the list of strictly increasing integers is a subtype of the list of distinct integers. To see why, observe that after applying the trivial top-level recursive refinement ρ_{\top} to the body, substituting the shapes, and applying the $[\text{<:-SUM}]$, $[\text{<:-PROD}]$ and $[\text{<:-REFLEX}]$ rules, the check reduces to (6).

Local Subtyping. Our recursive refinements represent universally quantified properties over the elements of a structure. Hence, we reduce subtyping between two recursively-refined structures to *local* subtype checks between corresponding elements of two *arbitrarily* chosen values of the recursive types. The judgment $[\text{<:-REC}]$ carries out this reduction via ρ -application and unfolding and enforces the local subtyping requirement with the final antecedent.

Typing. We now turn to the rules for type checking expressions. Rules $[\text{L-UNFOLD-M}]$ and $[\text{L-MATCH-M}]$ describe how values *extracted* from recursively constructed values are type checked. First, $[\text{L-UNFOLD-M}]$ is applied, which performs one unfolding of the recursive type, as described above. Second, the rule $[\text{L-MATCH-M}]$ is used on the resulting sum type. This rule stipulates that the entire expression has some type \hat{T} if the type is well-formed in the current environment, and that for each case of the match (i.e., for each element of the sum), the body expression has type \hat{T} in the environment extended with: (a) the corresponding match bindings, and, (b) the guard predicate that captures the relationship between the measure of the matched expression and the variables bound by the matched pattern.

Example. Consider the following function:

```
let rec sortcheck xs =
  match xs with
  | x :: (x' :: _ as xs') ->
    assert (x <= x'); sortcheck xs'
  | _ -> ()
```

Let Γ be $x_s : \alpha \text{ list}_{\leq}$. From rule $[\text{L-UNFOLD}]$, we have

$$\Gamma \vdash_{\text{Qunfold}} x_s : \text{Nil} + \text{Cons}\langle x : \alpha; x_s' : (\rho_{\leq}^x) \alpha \text{ list}_{\leq} \rangle$$

For clarity, we assume that the fresh names are those used in the match-bindings. For the Cons pattern in the outer match, we use the rule $[\text{L-MATCH-M}]$ to get the environment Γ' , which is Γ extended with $x : \alpha$, $x_s' : (\rho_{\leq}^x) \alpha \text{ list}_{\leq}$. Thus, $[\text{L-UNFOLD}]$ yields

$$\Gamma' \vdash_{\text{Qunfold}} x_s' : \text{Nil} + \text{Cons}\langle x' : \{ \nu : \alpha \mid x \leq \nu \}; x_s'' : \dots \rangle$$

Hence, in the environment:

$$x_s : \alpha \text{ list}_{\leq}; x : \alpha; x_s' : (\rho_{\leq}^x) \alpha \text{ list}_{\leq}; x' : \{ \nu : \alpha \mid x \leq \nu \}$$

which corresponds to the extension of Γ' with the (inner) pattern-match bindings, the argument type $\{ \nu = x \leq x' \}$ is a subtype of the input type $\{ \nu \}$ and system verifies that the `assert` cannot fail.

Rules $[\text{L-SUM-M}]$ and $[\text{L-FOLD-M}]$ describe how recursively constructed values are type checked. First, $[\text{L-SUM-M}]$ is applied, which uses the constructor C_j to determine to which sum type the tuple should be injected. Notice that, in the resulting sum: (a) the refinement predicate for all other sum elements is \perp , capturing the fact that C_j is the only inhabited constructor within the sum value, and, (b) for each measure m defined for the corresponding

recursive type, a predicate specifying the value of the measure m is conjoined to the refinement for the constructed sum. Second, the rule [L-FOLD-M] folds the (sum) type into a recursive type.

Example. Consider the expression `Cons(i, is)` which is returned by the function `range` from Figure 1. Let $\Gamma \doteq i:\text{int}; \text{is}:(\rho_{\leq}^{i+1}) \text{int list}_{\leq}$. Rule [L-REFINE] yields:

$$\Gamma; x'_1:\{i = \nu\} \vdash \{i + 1 \leq \nu\} <: \{x'_1 \leq \nu \wedge i \leq \nu\}$$

Consequently, using the rules for subtyping, we derive:

$$\Gamma; x'_1:\{i = \nu\} \vdash (\rho_{\leq}^{i+1}) \text{int list}_{\leq} <: (\rho') \text{int list}_{\leq} \quad (8)$$

where ρ' is $\langle\langle \cdot \rangle; \langle x'_1 \leq \nu \wedge i \leq \nu; \top \rangle\rangle$. Using [L-PROD]:

$$\Gamma \vdash_{\mathbb{Q}} (i, \text{is}) : \langle x'_1:\{i = \nu\}; x'_2:(\rho') \text{int list}_{\leq} \rangle$$

and so, using the subsumption rule [L-SUB] and (8) we have:

$$\Gamma \vdash_{\mathbb{Q}} (i, \text{is}) : \langle x'_1:\{i \leq \nu\}; x'_2:(\rho') \text{int list}_{\leq} \rangle$$

i.e., the first element of the pair is greater than i and the second element is an increasing list of values greater than than the first element and i . Thus, applying [L-SUM], we get:

$$\Gamma \vdash_{\mathbb{Q}} \text{Cons}(i, \text{is}) : \text{Nil} + \text{Cons} \langle x'_1:\{i \leq \nu\}; x'_2:(\rho') \text{int list}_{\leq} \rangle$$

As $(\rho_{\leq}^i) \text{int list}_{\leq}$ unfolds to the above type, [L-FOLD] yields

$$\Gamma \vdash_{\mathbb{Q}} \text{fold}(\text{Cons}(i, \text{is})) : (\rho_{\leq}^i) \text{int list}_{\leq}$$

i.e., `range` returns an increasing list of integers greater than i .

Example: Measures. Next, let us type check the below expression using the `elts` measure specification from Section 2.

```
let a = [] in let b = 1::a in
match b with x::xs -> () | [] -> assert false
```

Using [L-SUM-M] and [L-FOLD-M] (and [L-LET]), we derive:

$$\emptyset \vdash_{\mathbb{Q}} \text{Nil} : \{\text{elts } \nu = \text{empty}\} \\ a:\{\text{elts } \nu = \text{empty}\} \vdash_{\mathbb{Q}} \text{Cons}(1, a) : T_{1::a}$$

where $T_{1::a}$ abbreviates $\{\text{elts } \nu = \text{union}(\text{single } 1)(\text{elts } a)\}$. Due to [L-UNFOLD-M] and [L-MATCH-M], the `assert` in the `Nil` pattern-match body is checked in an environment Γ containing $a:\{\text{elts } \nu = \text{empty}\}$, $b:T_{1::a}$ and the guard $(\text{elts } b = \text{empty})$ from the definition of `elts` for `Nil`. Under this *inconsistent* Γ , the argument type $\{\text{not } \nu\}$ is a subtype of the input type $\{\nu\}$ and so the call to `assert` type checks, proving the `Nil` case is not reachable.

5. Polymorphic Refinements

We now describe how the language is extended to capture invariants of finite maps, by describing the syntax and static semantics of polymorphic refinements. The syntax of expressions and dynamic semantics are unchanged.

5.1 Syntax

Figure 9 shows how the syntax of types is extended to include polymorphically refined types (in addition to standard polymorphic types). First, type schemas can be universally quantified over *refined polytype variables* $\alpha(x:\tau)$. Intuitively, this refined polytype variable α can be instantiated with a refined type containing a free variable x of type τ . It is straightforward to extend the system to allow multiple free variables; we omit this for clarity. Second, the body of the type schema can contain *refined polytype variable instances* $\alpha[y/x]$. One way to view such instances is as the type where the program variable y takes the place of the free variable x in the refined type that α is instantiated with. That is, the instance is syntactic sugar for $\exists x.\{\nu:\alpha \mid y = x\}$.

B	::=	...	<i>Base:</i>
		$\alpha[y/x]$	refined polytype variable
$S(\mathbb{B})$::=	...	<i>Type Schema Schemas:</i>
		$\forall \alpha(x:\tau).S(\mathbb{B})$	refined polytype schema

Well-Formed Types

$$\frac{\alpha(x:\tau) \in \Gamma \quad \Gamma \vdash y:\tau}{\Gamma \vdash \alpha[y/x]} \text{ [WF-REFVAR]}$$

$$\frac{\Gamma; \alpha(x:\tau) \vdash S}{\Gamma \vdash \forall \alpha(x:\tau).S} \text{ [WF-REFPOLY]}$$

Decidable Subtyping

$$\frac{\alpha(x:\tau) \in \Gamma \quad \Gamma \vdash \{\nu:\tau \mid \nu = y_1\} <: \{\nu:\tau \mid \nu = y_2\}}{\Gamma \vdash \alpha[y_1/x] <: \alpha[y_2/x]} \text{ [L-REFVAR]}$$

Liquid Type Checking

$$\frac{\Gamma \vdash_{\mathbb{Q}} e : S \quad \alpha(x:\tau) \notin \Gamma \quad \Gamma \vdash \forall \alpha(x:\tau).S}{\Gamma \vdash_{\mathbb{Q}} [\Lambda \alpha(x:\tau)]e : \forall \alpha(x:\tau).S} \text{ [L-REFGEN]}$$

$$\frac{\Gamma; x:\tau_x \vdash T \quad \text{Shape}(T) = \tau \quad \Gamma \vdash_{\mathbb{Q}} e : \forall \alpha(x:\tau_x).S}{\Gamma \vdash_{\mathbb{Q}} [T/\alpha]e : S[T/\alpha]} \text{ [L-REFINST]}$$

Figure 9. Polymorphic Refinements: Syntax, Static Semantics

Example: [Finite Dependent Maps]. Consider the signature

```
create :: \forall \alpha, \beta(x:\alpha). \text{int} \rightarrow (i:\alpha, \beta[i/x]) \text{t}
set    :: \forall \alpha, \beta(x:\alpha). (i:\alpha, \beta[i/x]) \text{t} \rightarrow k:\alpha \rightarrow \beta[k/x] \rightarrow (j:\alpha, \beta[j/x]) \text{t}
get    :: \forall \alpha, \beta(x:\alpha). (i:\alpha, \beta[i/x]) \text{t} \rightarrow k:\alpha \rightarrow \beta[k/x]
mem    :: \forall \alpha, \beta(x:\alpha). (i:\alpha, \beta[i/x]) \text{t} \rightarrow k:\alpha \rightarrow \text{bool}
```

where t is a polymorphic type constructor that takes two arguments, that can be implemented in many ways. One possible implementation choice is using association lists, in which case $(i:\alpha, \beta[i/x]) \text{t}$ is an abbreviation for $(i:\alpha, \beta[i/x]) \text{list}$. In general, this signature can be implemented by any other data structure such as a balanced search trees, hash tables *etc.* The refined polytype schemas above specify that the corresponding functions implement the key operations of a *finite dependent map*. The schemas check that certain relationships hold between the keys and values when new elements are added to the map (using `set`), and they ensure that subsequent reads (using `get`) return values that satisfy the relationship. For example, the map $(i:\alpha, \beta[i/x]) \text{t}$ where α and β are instantiated with `int` and $\{\nu:\text{int} \mid x \leq \nu\}$ respectively, corresponds to a finite map where *for each* key-value pair, the value is greater than the key. For such a map, the above type for `set` ensures that when a new binding is added to the map, the value to be added has the type $\{x \leq \nu\}[k/x]$ which is $\{k \leq \nu\}$ i.e., is greater than the key k . Dually, when `get` is used to look up the map with a key k , the type specifies that the returned value is guaranteed to be greater than the query key k .

5.2 Static Semantics

Figure 9 summarizes the rules for polymorphic refinements.

Well-formedness. The well-formedness rules state that a polytype instance $\alpha[y/x]$ is well-formed if: (a) the instance occurs in a schema where quantified over $\alpha(x:\tau)$, i.e., where α can have a free occurrence of x of type τ , and (b) the free variable y that replaces x is bound in the environment to a type τ . It is straightforward to check that each of the schemas for `set`, `get`, *etc.* are well-formed.

Subtyping. To handle refined polytypes, we need extend our subtyping rules with a single rule for subtyping refined polytype variable instances. The intuition behind the rule follows from the

existential interpretation of the refined polytype instances and the fact that $\forall \nu. (\nu = y_1) \Rightarrow (\nu = y_2)$ implies $y_1 = y_2$, which implies $(\exists x. P \wedge x = y_1) \Rightarrow (\exists x. P \wedge x = y_2)$ for every logical formula P in which x occurs free. Thus, to check that the subtyping holds for any possible instantiation of α containing a free x , it suffices to check that the replacement variables y_1 and y_2 are equal.

Example. Let us see how the following implementation of `get`

```
let rec get xs k =
  match t with
  | [] -> diverge ()
  | (k', d') :: xs' -> if k' = k then d' else get xs' k
```

implements the refined polytype schema shown above. From the input assumption that `xs` has the type $\langle i : \alpha, \beta[i/x] \rangle \text{ list}$, and the rules for unfolding, pattern matching ([L-UNFOLD-M] and [L-MATCH-M]) we have that at the point where `d'` is returned, the environment Γ contains the type binding $d' : \beta[i/x][k/i]$ which, after telescoping the substitutions, is equivalent to the binding $d' : \beta[k/x]$. Due to [L-IF], the branch condition $k' = k$ is in Γ , and so $\Gamma \vdash \{\nu : \alpha \mid \nu = k\} <: \{\nu : \alpha \mid \nu = k'\}$. Thus, from rule [L-REFVAR], and subsumption, we derive that the `then` branch has the type $\beta[k/x]$ from which the judgment follows.

Typing. Finally, the rules for polymorphic generalization ([L-REFGEN]) and instantiation ([L-REFINST]) are extended to handle refined polytype variables and instances respectively. There are two points that are worth noticing. First, observe that a refined polytype variable $\alpha \langle x : \tau \rangle$ can be instantiated with a dependent type T that contains a free occurrence of x of type τ . This is ensured by altering the well-formedness antecedent for the instantiation rule to check that T is well-formed under the environment extended with the appropriate binding for x . However, once T is substituted into the body of the schema S , the different pending substitutions at each of the refined polytype instances of α are applied and hence x does not appear free in the instantiated type, which is consistent with the existential interpretation of polymorphic refinements.

Polymorphic Refinements vs. Array Axioms. Our technique of specifying the behavior of finite maps using polymorphic refinements is orthogonal to, and can be combined with, the classical approach that uses McCarthy’s *array axioms*. In this approach, one models reads and writes to arrays, or more generally, finite maps, using two operators. The first, $Sel(m, i)$, takes a map m and an address i and returns the value stored in the map at that index. The second, $Upd(m, i, v)$, takes a map m , an index i and a value v and returns the new map which corresponds to m updated at the index i with the new value. Using these operators, an analysis can algorithmically reason about the exact contents of specific, named addresses within a map. For example, the formula $Sel(0, i) = 0$ specifies that 0 maps the address i to the value 0. However, to capture invariants that hold for all elements in the map one must use universally quantified formulas, which make algorithmic reasoning brittle and unpredictable. In contrast, polymorphic refinements can smoothly capture and reason about the relationship between all the addresses and values in a map, but do not, as described so far, allow us to refer to particular named addresses. However, in our system, we can combine the best of both worlds, i.e., reason about both the general relationships between keys and values in a map, as well as the specific value of a particular key, by using using the operators to refine the output types of `set` and `get`, with the predicates $(\nu = Upd(m, k, v))$ and $(\nu = Sel(m, k))$ respectively.

Example: [Mutable Linked Structures.] Polymorphic refinements can also be used to verify properties of linked structures, as each link field corresponds to a map from the set of source structures to the set of target structures. For example, a field `f` can be encoded by a map `f`, a *field read from* `x.f` corresponds to `get f x`, and a

field write of `e` to `x.next` corresponds to `set f x e`. Consider this SSA-converted [2] implementation of the textbook `find` function for the *union-find* data structure.

```
let rec find rank parent0 x =
  let px = get parent0 x in
  if px = x then (parent0, x) else
    let (parent1, px') = find rank parent0 px in
    let parent2 = set parent1 x px' in
    (parent2, px')
```

The function `find` takes two maps as input: `rank` and `parent0`, corresponding to the rank and parent fields in an imperative implementation, and an element `x`, and finds the “root” of `x` by transitively following the `parent` link, until it reaches an element that is its own parent. The function implements *path-compression*, i.e., it (destructively) updates the parent map so that subsequent queries jump straight to the parent. The data structure maintains the acyclicity invariant that each non-root element’s rank is strictly smaller than the rank of the element’s parent. This invariant is verified by checking that the output parent map has type:

$$(i : \text{int}, \{\nu : \text{int} \mid (i = \nu) \vee Sel(\text{rank}, i) < Sel(\text{rank}, \nu)\}) t$$

which captures the acyclicity invariant by stating that for each key i , either the key is its own parent or the key’s rank is less than the parent’s rank. Our system verifies that when `find` is called with a parent map that satisfies the invariant, the output map also satisfies the invariant. To do so, it automatically instantiates the refined polytype variables in the signatures for `get` and `set` with the appropriate refinement types, after which the rules from Section 3 suffice to establish the invariant. Similarly, our system verifies the `union` function where the rank of a root is incremented when two roots of equal ranks are linked. Thus, polymorphic refinements and liquid type inference combine to verify complex acyclicity invariants of mutable data structures.

6. Evaluation

We have implemented type-based data structure verification in DSOLVE, which takes as input an OCAML program (a `.ml` file), a property specification (a `.mlq` file), and a set of logical qualifiers (a `.quals` file). The program corresponds to a module, and the specification comprises measure definitions and types against which the interface functions of the module should be checked. DSOLVE combines the manually supplied qualifiers (`.quals`) with qualifiers scraped from the properties to be proved (`.mlq`) to obtain the set \mathbb{Q} used to infer types for verification. DSOLVE produces as output the list of possible refinement type errors, and a `.annot` file containing the inferred liquid types for all the program expressions.

Benchmarks. We applied DSOLVE to the following set of benchmarks, designed to demonstrate: *Expressiveness*—that our approach can be used to verify a variety of complex properties across a diverse set of data structures, including textbook structures, and structures designed for particular problem domains; *Efficiency*—that our approach scales to large, realistic data structure implementations; and *Automation*—that due to liquid type inference, our approach requires a small set of manual qualifier annotations.

- **List-sort:** a collection of textbook list-based sorting routines, including insertion-sort, merge-sort and quick-sort,
- **Map:** an ordered AVL-tree based implementation of finite maps, (from the OCAML standard library)
- **Ralist:** a random-access lists library, (due to Xi [19])
- **Redblack:** a red-black tree library, (due to Dunfield [7])
- **Stablesort:** a tail recursive mergesort, (from the OCAML standard library)
- **Vec:** a tree-based vector library (due to de Alfaro [5])

Program	LOC	Ann.	T(s)	Property
List-sort	111	7	5	Sorted, Elts
Map	98	14	25	Balance, BST, Set
Ralist	92	3	2	Len
Redblack	106	2	29	Balance, Color, BST
Stablesort	124	1	4	Sorted
Vec	343	9	87	Balance, Len1, Len2
Heap	122	6	33	Heap, Min, Set
Splayheap	134	3	6	BST, Min, Set
Malloc	71	2	2	Alloc
Bdd	206	3	80	VariableOrder
Unionfind	65	2	5	Acyclic
Subvsolve	264	2	20	Acyclic
Total	1736	54	300	

Figure 10. Results: LOC is the number of lines of code without comments, **Property** is the properties verified, **Ann.** is the number of manual (qualifier) annotations, and **T(s)** is the time in seconds, DSOLVE requires to verify each property.

- Heap: a binary heap library, (due to Filliâtre [8])
- Splayheap: a splay tree based heap, (due to Okasaki [15])
- Malloc: a resource management library,
- Bdd: a binary decision diagram library (due to Filliâtre [8])
- Unionfind: the textbook union-find data structure,
- Subvsolve: a DAG-based bit-level inference algorithm [11]

On the programs, we check the following properties: Sorted, the output list is sorted, Elts, the output list has the same elements as the input, Balance, the output trees are balanced, BST, the output trees are binary search ordered, Set, the structure implements a set interface, e.g., the outputs of the `add`, `remove`, `merge` functions correspond to the addition of, removal of, union of, (resp.) the elements or sets corresponding to the inputs, Len, the various operations appropriately change the length of the list, Color, the output trees satisfy the red-black color invariant, Heap, the output trees are heap-ordered, Min, the `extractmin` function returns the smallest element, Alloc, the used and free resource lists only contain used and free resources (requires the invariant that the lists have no *duplicates* [1]), VariableOrder, the output BDDs have the variable ordering property, Acyclic, the output graphs are acyclic.

Results. The results of running DSOLVE on the benchmarks are summarized in Figure 6. Even for the larger benchmarks, very few qualifiers are required for verification. These qualifiers capture simple relationships between variables that are not difficult to specify after understanding the program. Due to liquid type inference, the total amount of manual annotation remains extremely small — just 4% of code size, which is acceptable given the complexity of the implementation and the properties being verified. Next, we describe the subtleties of some of the benchmarks and describe how DSOLVE verifies the key invariants. See [1] for more details.

Sorting. We used DSOLVE to verify that implementations of various list sorting algorithms returned sorted lists whose elements were the same as the input lists’, i.e., that the sorting functions had type $xs:\alpha \text{list} \rightarrow \{\nu:\alpha \text{list}_{\leq} \mid \text{elts } \nu = \text{elts } xs\}$. We checked the above properties on `insertsort` (shown in Figure 2), `mergesort` [19] which recursively halves the lists, sorts, and merges the results, `mergesort2` [19] which chops the list into a list of (sorted) lists of size two, and then repeatedly passes over the list of lists, merging adjacent lists, until it is reduced to a singleton which is the fully sorted list, `quicksort`, which partitions the list around a pivot, then sorts and appends the two partitions, `Stablesort` from the OCAML standard library’s `List` module, which is a tail-recursive mergesort that uses two mutually recursive

functions, one which returns an increasing list, another a decreasing list. For each benchmark, DSOLVE infers that the sort function has type Sorted using only the qualifier $\{\nu \leq \star\}$. To prove Elts, we need a few simple qualifiers relating the elements of the output list to those of the input. DSOLVE cannot check Elts for `stablesort` due to its (currently) limited handling of OCAML’s pattern syntax.

Maps. We applied DSOLVE to verify OCAML’s tree-based functional Map library. The trees have the ML type:

```
type ('a, 'b) t =
  E | N of 'a * 'b * ('a, 'b) t * ('a, 'b) t * int
```

The `N` constructor takes as input a key of type `'a`, a datum of type `'b`, two subtrees, and an integer representing the *height* of the resulting tree. The library implements a variant of AVL trees where, internally, the heights of siblings can differ by at most 2. A *binary search tree* (BST) is one where, for each node, the keys in the left (resp. right) subtree are smaller (resp. greater) than the node’s key. A tree is *balanced* if, at each node, the heights of its subtrees differ by *at most* two. Formally, after defining a height measure `ht`:

```
measure ht = E -> 0 | N (_,_,l,r,_) ->
  if ht l < ht r then 1 + ht r else 1 + ht l
```

the balance and BST invariants are respectively specified by:

$$(\rho_{\text{bal}}) \mu t. E + N(k:\alpha, d:\beta, l:t, r:t, h:\text{int}) \quad (\text{Balance})$$

$$\mu t. E + N(k:\alpha, d:\beta, l:(\rho_{<}) t, r:(\rho_{>}) t, h:\text{int}) \quad (\text{BST})$$

$$\rho_{>} \doteq \langle \langle \rangle; \langle \nu \bowtie k; \top; \top; \top; \top \rangle \text{ for } \bowtie \in \{<, >\}$$

$$\rho_{\text{bal}} \doteq \langle \langle \rangle; \langle \top; \top; \top; e_b; e_h \rangle \rangle$$

$$e_h \doteq (\text{ht } l < \text{ht } r) ? (\nu = 1 + \text{ht } r) : (\nu = 1 + \text{ht } l)$$

$$e_b \doteq (\text{ht } l - \text{ht } \nu \leq 2) \wedge (\text{ht } \nu - \text{ht } l \leq 2)$$

DSOLVE verifies that all trees returned by API functions are balanced binary search trees, that no programmer-specified assertion fails at run-time, and that the library implements a set interface.

Vectors. We applied DSOLVE to verify various invariants in a library that uses binary trees to represent C++-style extensible vectors [5]. The i^{th} element of the vector is the root if the size of the left tree is i , the i^{th} element of the left tree if i is less than the number of elements of the left tree, and the $(i - n - 1)^{\text{th}}$ element of the right tree if the left tree has n elements. To ensure various operations are efficient, the heights of the subtrees at each level can differ by at most two. DSOLVE verifies that that all the trees returned by API functions, which include appending to the end of a vector, updating values, deleting sub-vectors, concatenation, *etc.*, are balanced, vector operations performed with valid index operands, i.e., indices between 0 and the number of elements in the vector don’t fail (Len1), and that all functions passed as arguments to the iteration, fold and map procedures are called with integer arguments in the appropriate range (Len2). DSOLVE found a subtle bug in the rebalancing procedure; by using the inferred types we found a minimal and complete fix which the author adopted.

Binary Decision Diagrams. A *Binary Decision Diagram* (BDD) is a reduced decision tree used to represent boolean formulas. Each node is labeled by a propositional variable drawn from some ordered set $x_1 < \dots < x_n$. The nodes satisfy a *variable ordering* invariant that if a node labeled x_i has a child labeled x_j then $x_i < x_j$. A combination of hash-consing/memoization and *variable ordering* ensures that each formula has a canonical BDD representation. Using just three elementary qualifiers, DSOLVE verifies the variable ordering invariant in an OCAML BDD library implementation by Filliâtre [8]. The verification requires recursive refinements to handle the ordering invariant and polymorphic refinements to handle the memoization that is crucial for efficient implementations.

Bit-level Type Inference. We applied DSOLVE to verify an implementation of a graph-based algorithm for inferring bit-level types

from the bit-level operations of a C program [11]. The bit-level types are represented as a sequence of *blocks*, each of which is represented as a node in a graph. Mask or shift operations on the block cause the block to be *split* into sub-blocks, which are represented by the list of successors of the block node. Finally, the fact that value-flow can cause different bit-level types to have unified subsequences of blocks is captured by having different nodes *share* successor blocks. The key invariant maintained by the algorithm is that the graph contains no cycles. DSOLVE combines recursive and polymorphic refinements to verify that the graph satisfies an acyclicity invariant like *DAG* from (3), in Section 2.2.

7. Related Work

Indexed Type based approaches use types augmented with *indices* which capture invariants of recursive types [20, 4, 7]. Recursive refinements offer several significant advantages over indices. First, they are strictly more expressive. While any index can be directly encoded with measures, it is unclear if canonical invariants like sortedness, distinctness (`int list≠`), and binary-search-ordering can be easily captured with indices. Consequently, only 8 (out of 23) of the properties verified in Figure 6 can be verified using indices. Further, the properties captured by polymorphic refinements cannot be expressed as indices. Second, indices “bake” the invariants into the type definition, thereby breaking abstraction; the programmer would have to write different functions for refined and unrefined versions of a type. For example, the programmer would have to write different copies of `map`, `fold`, and `toString` for sorted and unsorted lists. Third, no inference algorithm for indexed types has been demonstrated, which greatly limits their usability due to the drastically increased annotation burden; the programmer must annotate *all* functions and polymorphic instantiations. For example, at each call to `Hashtbl.add`, the programmer would have to specify the appropriate indexed type instantiations for the type variables α , β . In contrast, by *separating* the refinements from the underlying types, and *uniformly* representing refinements as conjunctions of qualifiers, our system permits inference, which is crucial for ensuring the usability of the system.

Hoare Logic based approaches require that the programmer write pre- and post-conditions and loop invariants for functions in a rich, higher-order specification logic. From these and the code, *verification conditions* (VCs) are generated whose validity implies that the code meets the specification. The VCs are then proved using automatic [12] or interactive theorem proving [14, 22, 17]. These approaches allow for the specification of far more expressive properties than is possible in our system. However, they require significantly more manual effort in interacting with the prover. Of the above proposals, the latter three have equal or more expressiveness than our approach, but require, at a conservative estimate, annotations amounting to more than three times the code size to prove similar properties. A direct comparison is difficult as [14, 22] consider lower-level languages and [17] considers some different properties. Nevertheless, the order-of-magnitude annotation reduction proves the utility of our approach.

Abstract Interpretation based approaches focus on inferring lower-level *shape* properties (e.g., that a structure is a singly-linked list or tree) in the presence of destructive heap updates. These techniques work by carefully controlling generalization (i.e., “blurring”) and instantiation (i.e., “focusing”) using a combination of user-defined recursive predicates [13, 21] and abstract domains tailored to the structure being analyzed [10, 3]. Our insight is that in high-level languages, shape invariants can be guaranteed using a rich type system. Furthermore, by piggybacking refinements on top of the types, one can use abstract interpretation (in the form of liquid type inference) to verify properties which have hitherto been

beyond the scope of automation. In future work we would like to apply our techniques to lower-level languages, by first using shape analysis to reconstruct the recursive type information, and then using recursive and polymorphic refinements over the reconstructed shapes to verify high-level properties.

Conclusions

Our refinement type-based approach is a sweet spot in the expressiveness-automation spectrum — it allows the verification of rich properties with an extremely low annotation burden. Our key insight is that the challenging work of generalizing and instantiating rich universal invariants can be automated by piggybacking simple refinement predicates on top of types, and carefully dividing the labor of analysis between the type system and an SMT solver. Of course, our approach does not make verification *easy* — the data structures we verify are highly nontrivial and difficult to implement correctly. However we make verification significantly *easier* as the programmer need only write down a small set of simple qualifiers that capture relevant relationships. We have demonstrated the utility of our approach by using DSOLVE to verify a diverse set of heavily used data structure implementations. Although we had to understand the code and the comments to determine the key invariants, our type-based approach reduces the manual annotations required for verification by more than an order of magnitude.

References

- [1] Anonymous. Please contact PC chair for details.
- [2] A. W. Appel. SSA is functional programming. *SIGPLAN Notices*, 33(4), 1998.
- [3] B. E. Chang and X. Rival. Relational inductive shape analysis. In *POPL*, pages 247–260, 2008.
- [4] S. Cui, K. Donnelly, and H. Xi. Ats: A language that combines programming with theorem proving. In *FroCos*, 2005.
- [5] Luca de Alfaro. Vec: Extensible, functional arrays for ocaml. <http://www.dealfaro.com/vec.html>.
- [6] L. de Moura and N. Bjørner. Z3: An efficient smt solver. In *TACAS*, pages 337–340, 2008.
- [7] Joshua Dunfield. *A Unified System of Type Refinements*. PhD thesis, Carnegie Mellon University, Pittsburgh, PA, USA, 2007.
- [8] J.C. Filliâtre. Ocaml software. <http://www.lri.fr/filliâtre/software.en.html>.
- [9] C. Flanagan. Hybrid type checking. In *POPL*. ACM, 2006.
- [10] S. Gulwani, B. McCloskey, and A. Tiwari. Lifting abstract interpreters to quantified logical domains. In *POPL*, pages 235–246, 2008.
- [11] R. Jhala and R. Majumdar. Bit-level types for high-level reasoning. In *FSE*. ACM, 2006.
- [12] S. K. Lahiri and S. Qadeer. Back to the future: revisiting precise program verification using smt solvers. In *POPL*, 2008.
- [13] T. Lev-Ami and S. Sagiv. TVLA: A system for implementing static analyses. In *SAS*, LNCS 1824, pages 280–301. Springer, 2000.
- [14] A. Nanevski, G. Morrisett, A. Shinnar, P. Govereau, and L. Birkedal. Ynot: Reasoning with the awkward squad. In *ICFP*, 2008.
- [15] C. Okasaki. *Purely Functional Data Structures*. CUP, 1999.
- [16] B. C. Pierce. *Types and Programming Languages*. MIT Press, 2002.
- [17] Y. Regis-Gianas and F. Pottier. A Hoare logic for call-by-value functional programs. In *MPC*, 2008. To appear.
- [18] P. Rondon, M. Kawaguchi, and R. Jhala. Liquid types. In *PLDI*, pages 158–169, 2008.
- [19] H. Xi. DML code examples. <http://www.cs.bu.edu/fac/hwxi/DML/>.
- [20] H. Xi and F. Pfenning. Dependent types in practical programming. In *POPL*, pages 214–227, 1999.
- [21] H. Yang, J. Berdine, C. Calcagno, B. Cook, D. Distefano, and P. W. O’Hearn. Scalable shape analysis for systems code. In *CAV*, 2008.
- [22] K. Zee, V. Kuncak, and M. C. Rinard. Full functional verification of linked data structures. In *PLDI*, pages 349–361, 2008.